



# Opportunities and Challenges of Next Generation Power Electronics for Grid Applications

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Northeastern



Rensselaer



TUSKEGEE  
UNIVERSITY

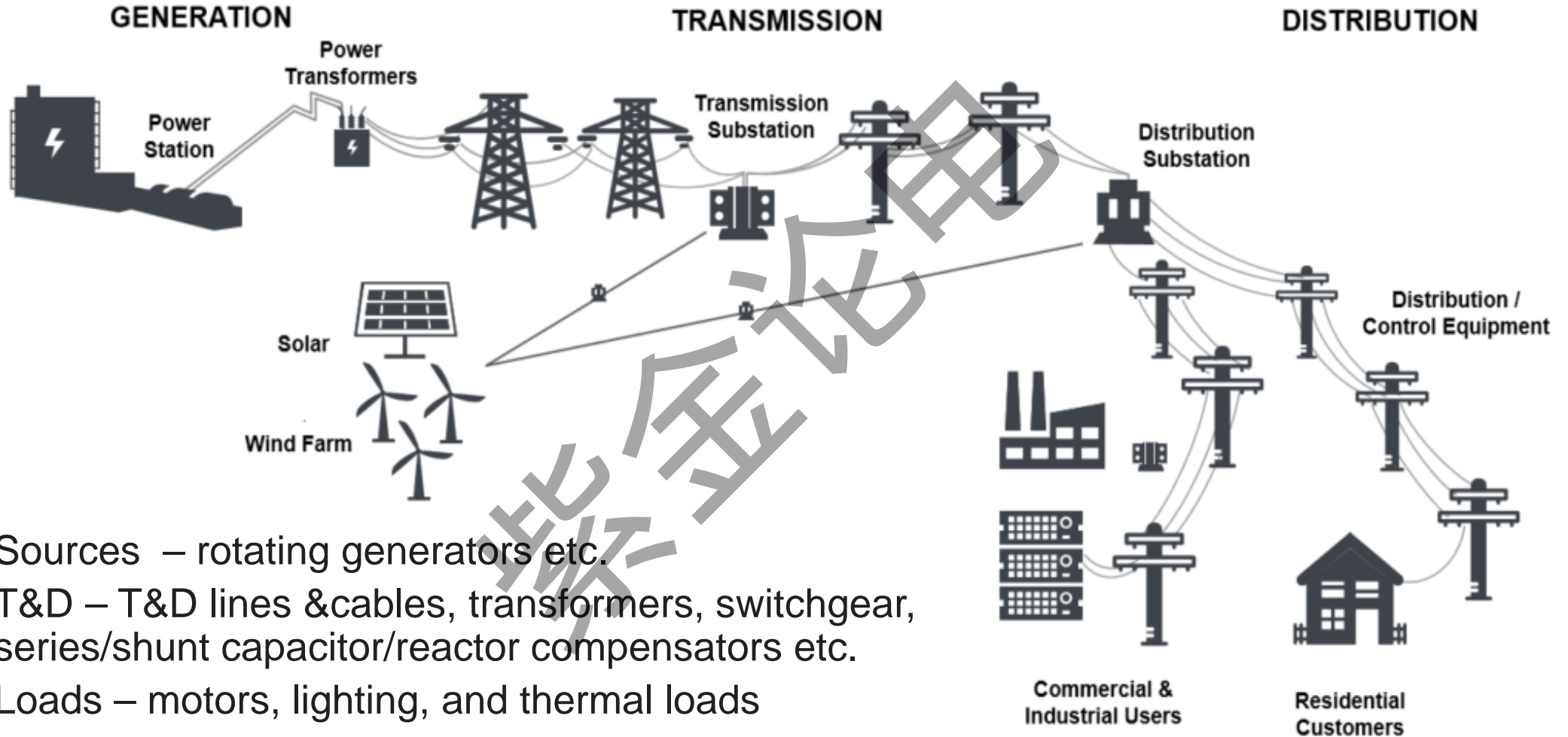
# Outline

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- Grid power Electronics Overview and Emerging Needs
- WBG Semiconductors and Opportunities
- New Challenges and Research Needs

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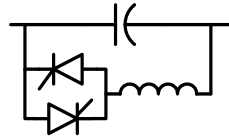
# Conventional Power Grid Equipment



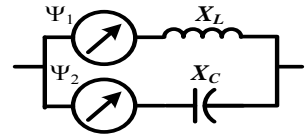
- Sources – rotating generators etc.
- T&D – T&D lines & cables, transformers, switchgear, series/shunt capacitor/reactor compensators etc.
- Loads – motors, lighting, and thermal loads
- Limited power electronics

# Power Electronics for AC Transmission – FACTS

- Series connected compensators)

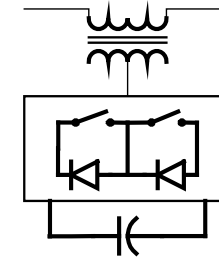


TCSC



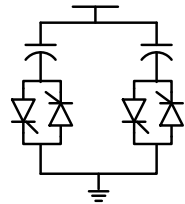
$\Psi_1$  and  $\Psi_2$  are phase-shift elements

IPC

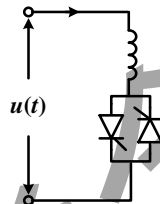


SSSC

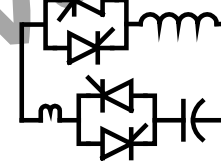
- Shunt connected compensators)



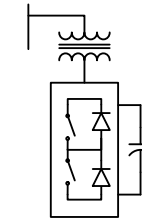
TSC



TSR

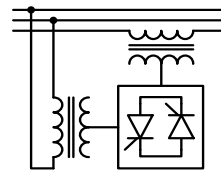


SVC

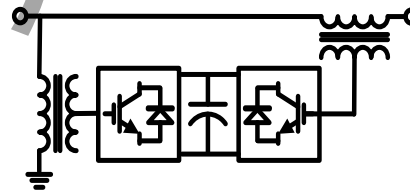


STATCOM

- Series and shunt connected compensators



TCPST

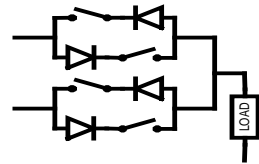


UPFC

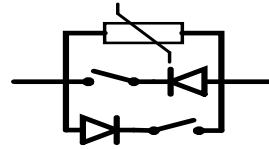
**Conventional FACTS  
based on thyristor  
technology, with limited  
performance and  
capabilities**

# Power Electronics for AC Distribution – Custom Power

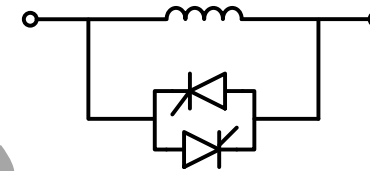
- Power flow control and interruption



SSTS

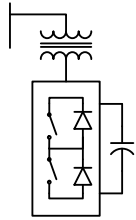


SSCB

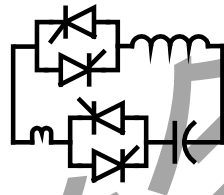


SSFCL

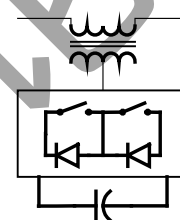
- Power system conditioning and compensation



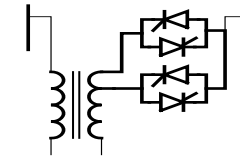
DSTATCOM



SVC

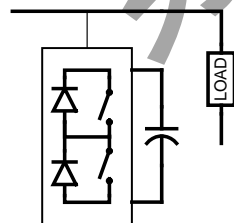


DVR

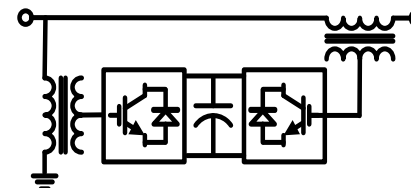


TCVR

- Power quality enhancement



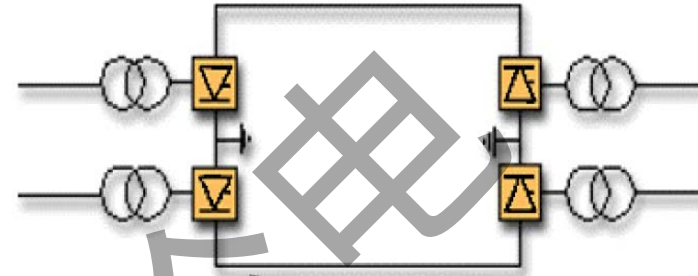
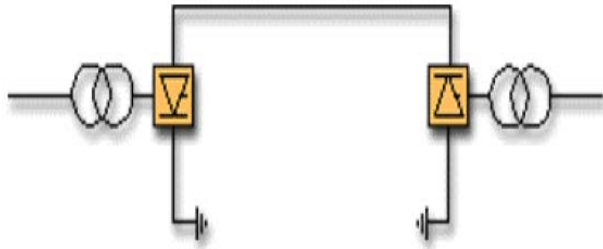
Shunt AF



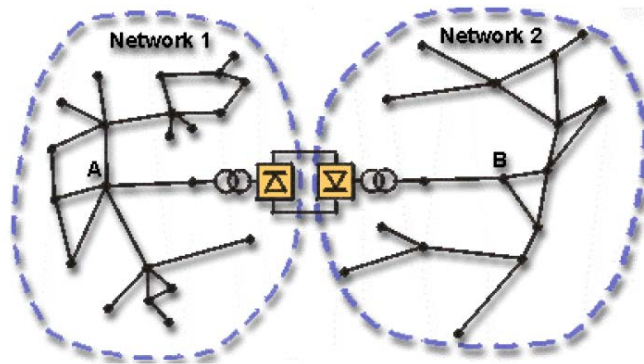
UPQC

# High Voltage DC (HVDC) Transmission

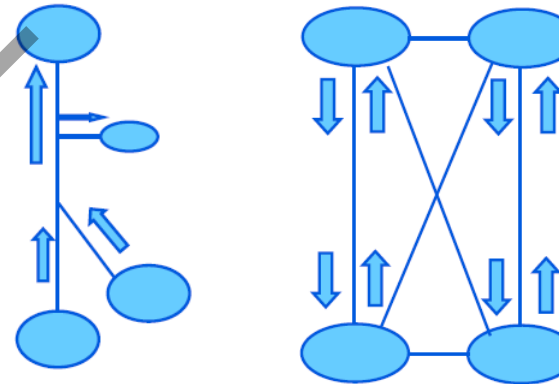
- Point to point transmission



- Back-to-back system

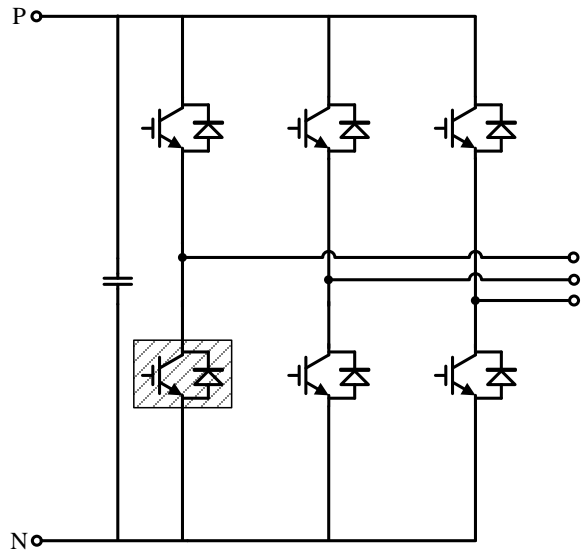


- Multi-terminal system

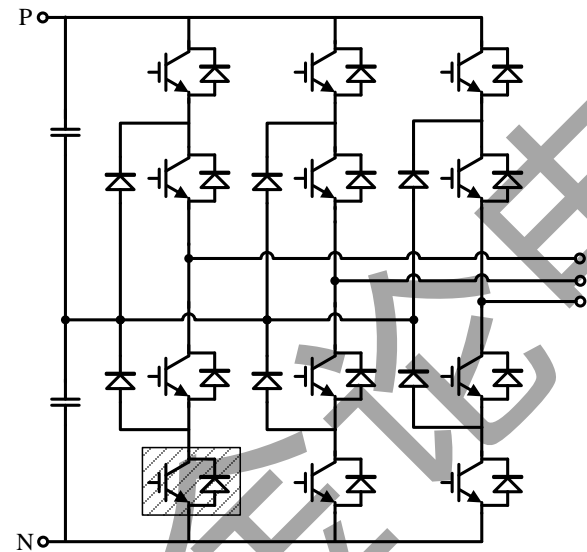


**HVDC more economical for long-distance transmission; HVDC can decouple dynamics of AC systems, benefiting system stability and protection**

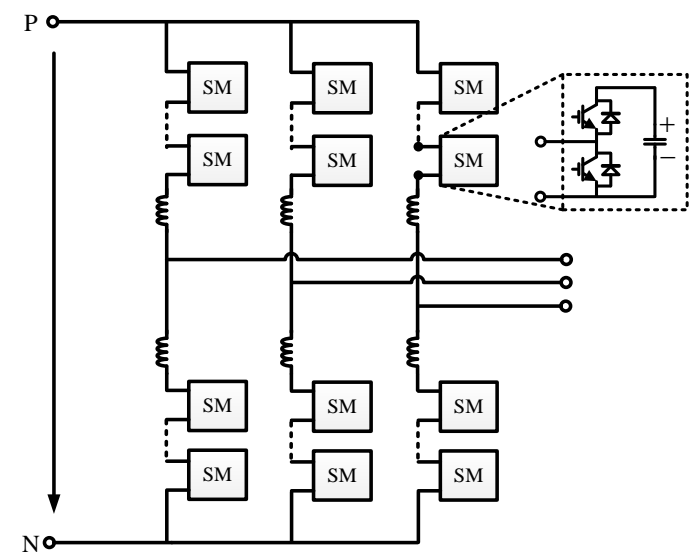
# HVDC Technology Evolution: VSC Topologies



Two-level



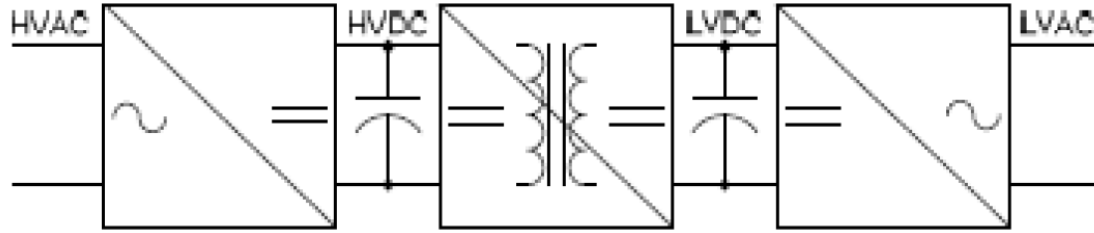
Three-level (NPC)



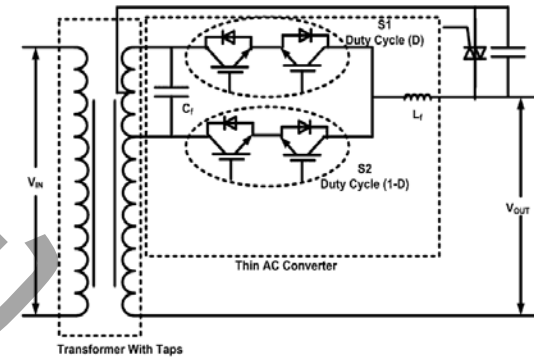
Modular multi-level converter (MMC)

- VSC based on switching devices (IGBT/IGCT), with better performance than thyristor based LCC, and significantly converter station footprint and less right-of-way
- MMC latest generation of VSC topology: low loss, and avoid series devices.

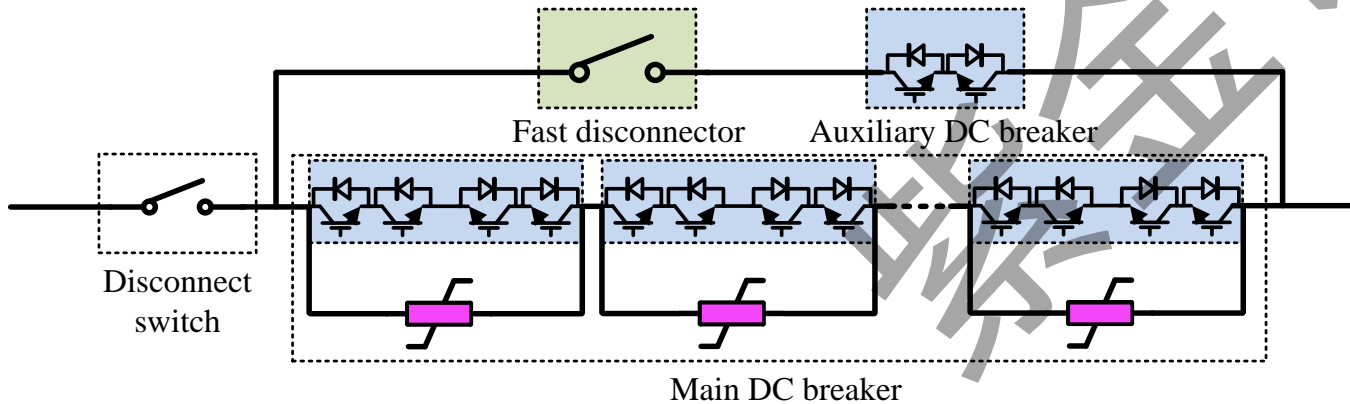
# Grid Power Electronics – More Recent Development



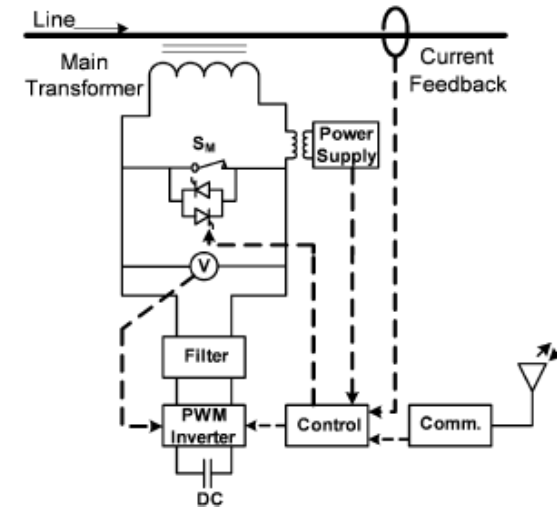
**固态变压器**  
Solid-state Transformer



**可控网络变压器**  
Controllable Network Transformer



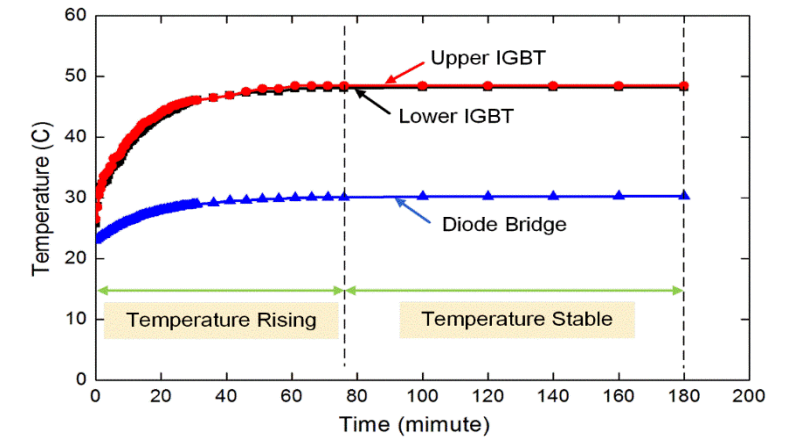
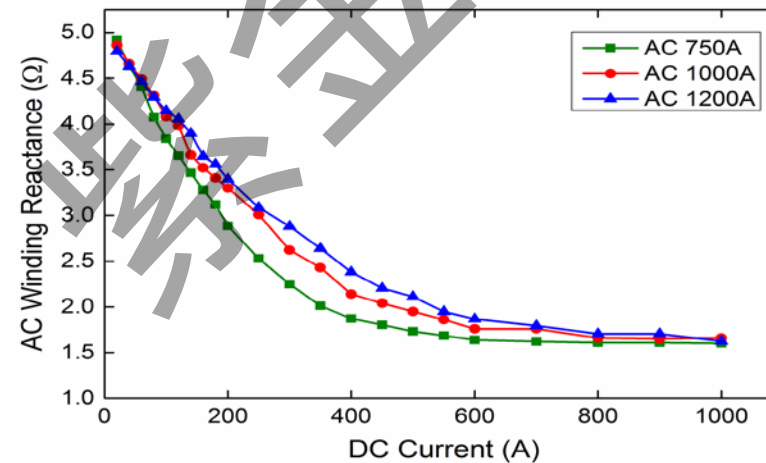
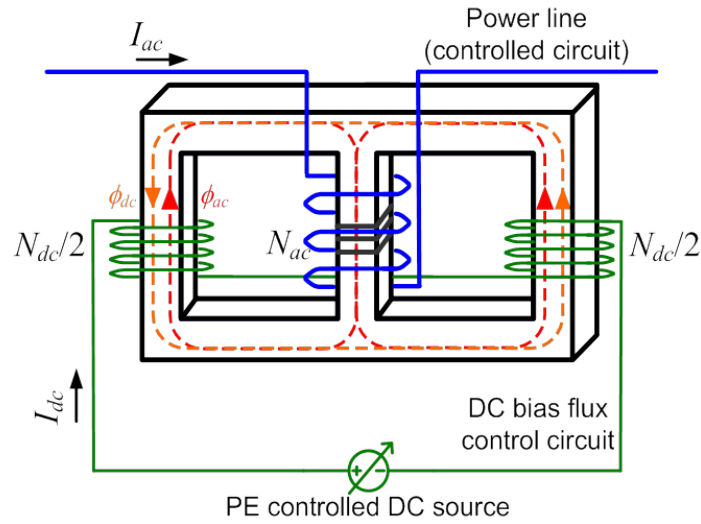
**混合型直流断路器**  
Hybrid DC Circuit Breaker



**分布式静态串联补偿器**  
Distributed Static Series Compensator

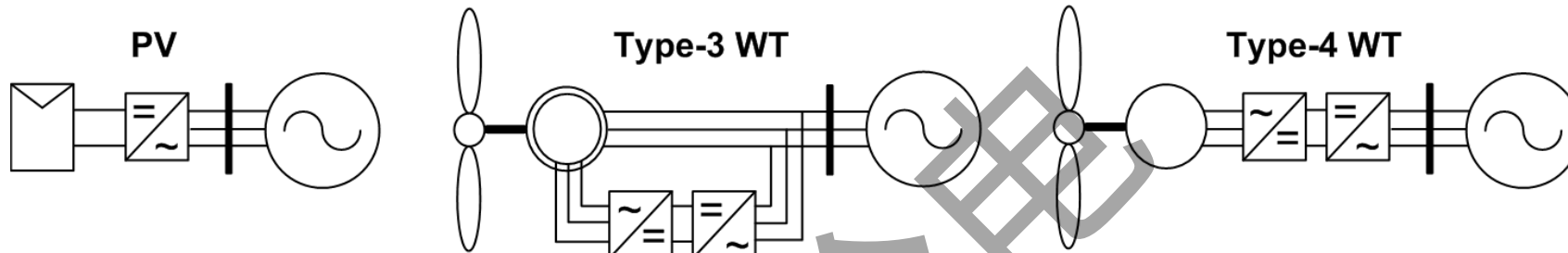


# Grid Power Electronics – More Recent Development

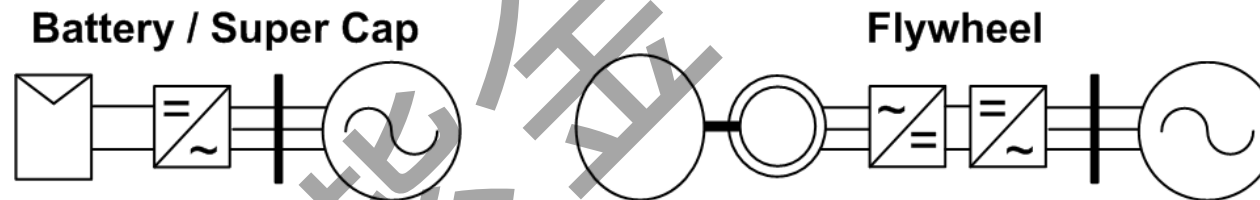


# Grid Power Electronics – Emerging Needs

- Renewable energy source interface

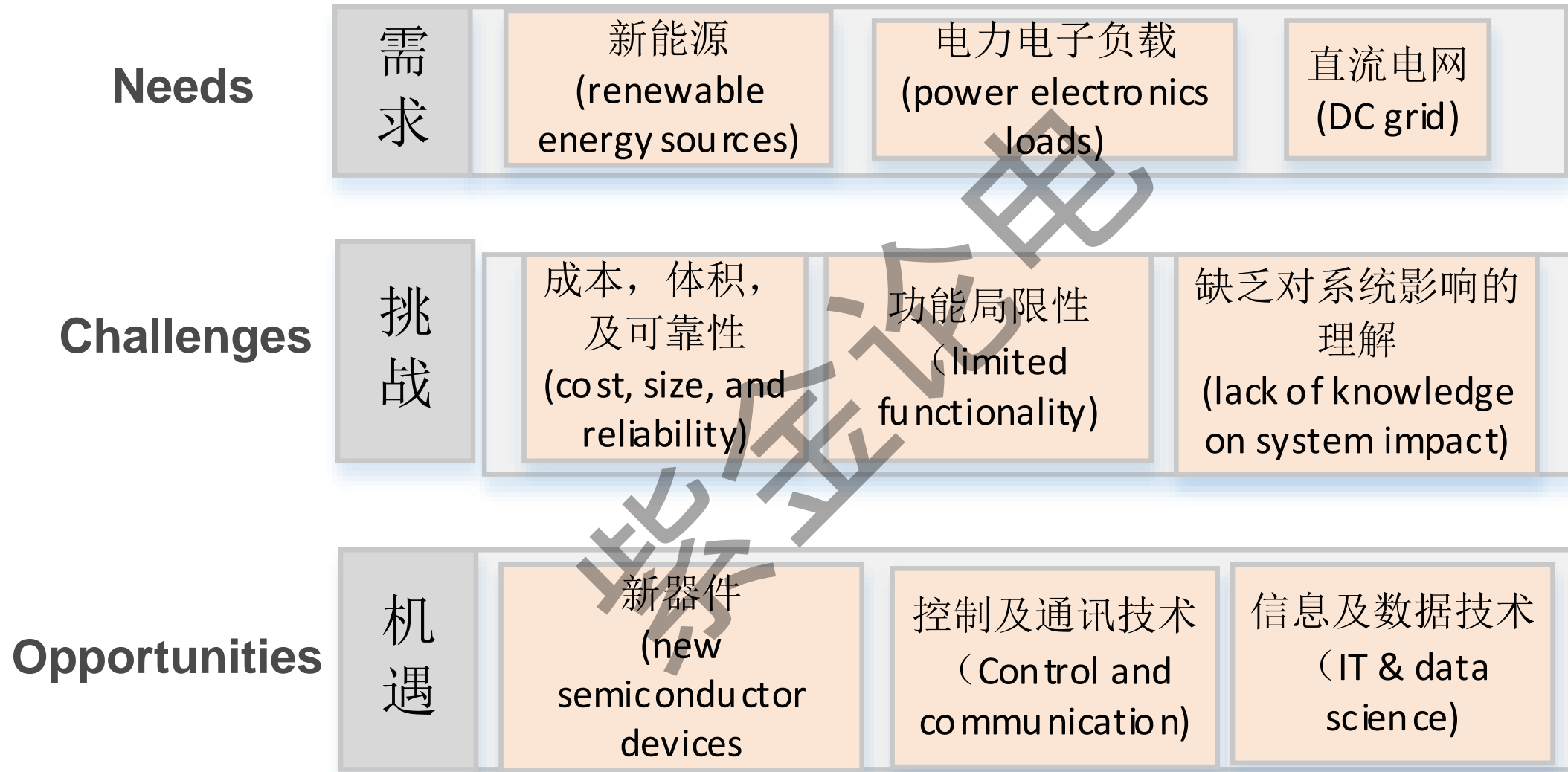


- Energy storage systems

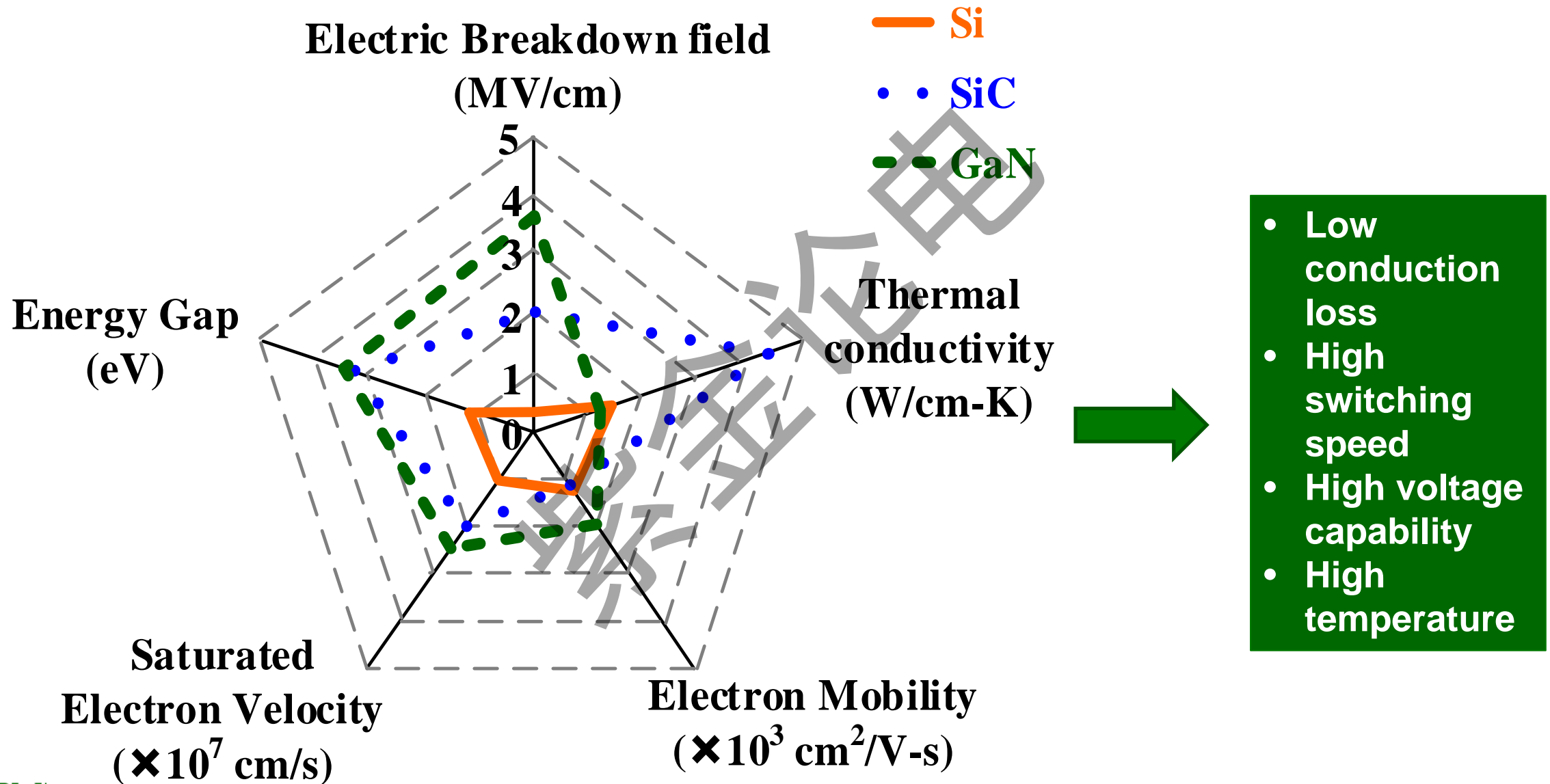


- Power electronic loads: data center, EV charging station, large motor drive
- Multi-terminal HVDC, microgrid (AC or DC)

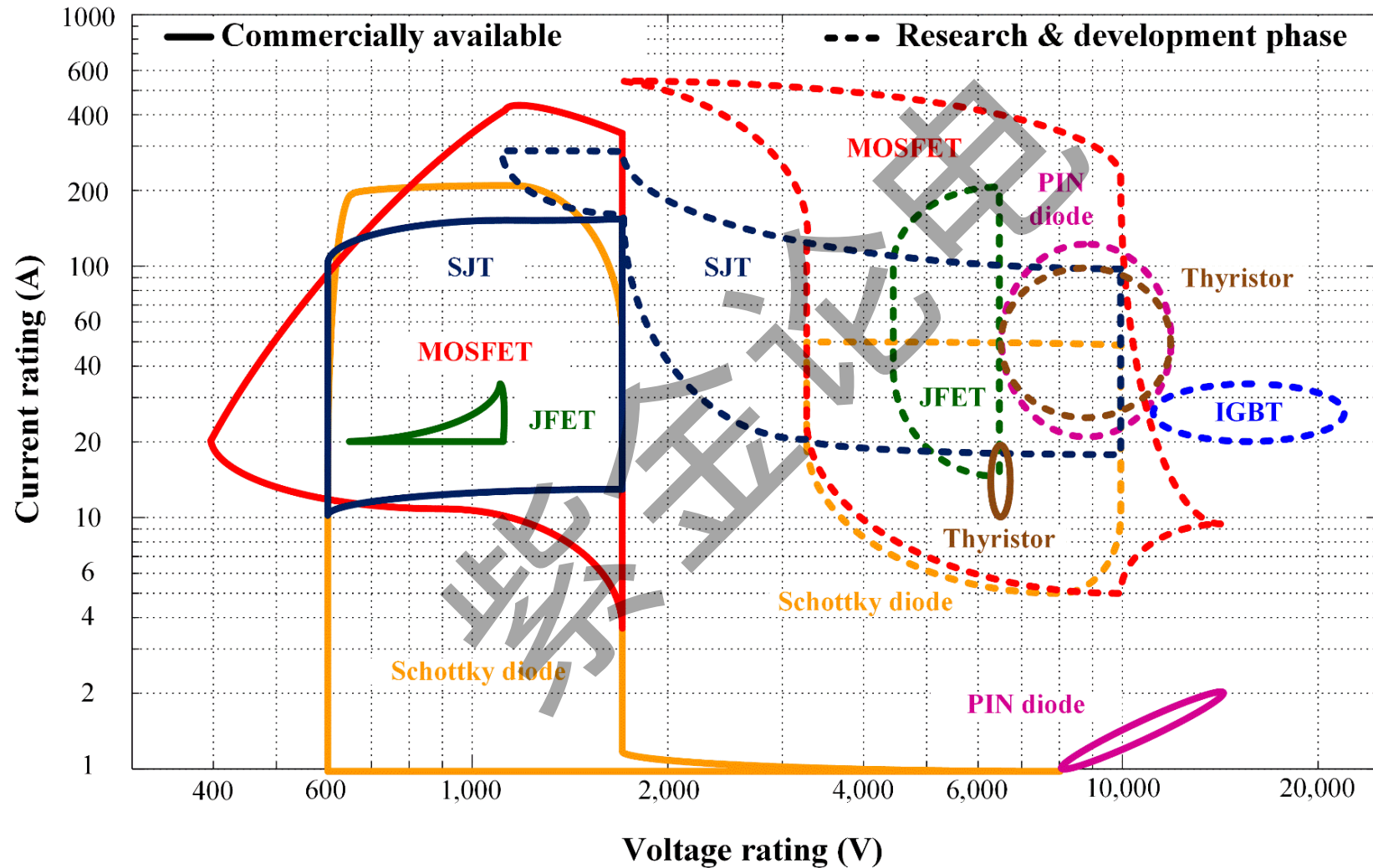
# Needs, Challenges, and Opportunities



# Properties of Wide Bandgap Semiconductors



# State-of-the-art SiC Devices



# (High Voltage) WBG Device Applications

## ❑ Wide band-gap (WBG) vs. Silicon

- High breakdown electric field, high voltage rating, low conduction loss
- Fast switching speed, high switching frequency
- Superior thermal characteristics

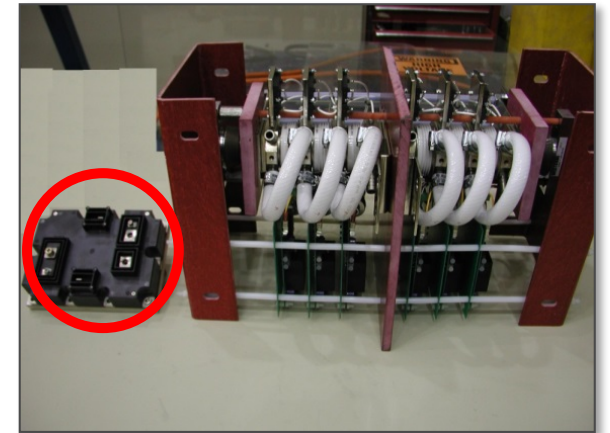
## ❑ Applications should take advantages of

- Low loss
- Fast switching speed
- High frequency application

## ❑ Benefits of HV SiC can be realized in several ways

- Direct substitution – improved efficiency and power density
- Simplified topology – further loss reduction and increased power density
- Enable new capability and functionality for system-level
- Enable new applications or replace the non-PE equipment

**SiC vs. Silicon**  
1MW Comparison of  
Switches



Wolfspeed 10 kV/240A  
SiC MOSFET

# WBG Potential Applications in Grid

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- Improve the performance of the existing PE equipment
  - Efficiency improvement for all;
  - density improvement for some;
  - performance improvement (e.g. circuit breakers)
- Replace non-PE equipment
  - Solid-state Transformer,
  - solid-state circuit breaker/current limiter
- Enhance functionality/capability
  - Smart inverter
- Enable new applications
  - DC grid
  - high bandwidth conditioner
  - direct-tied PV inverter

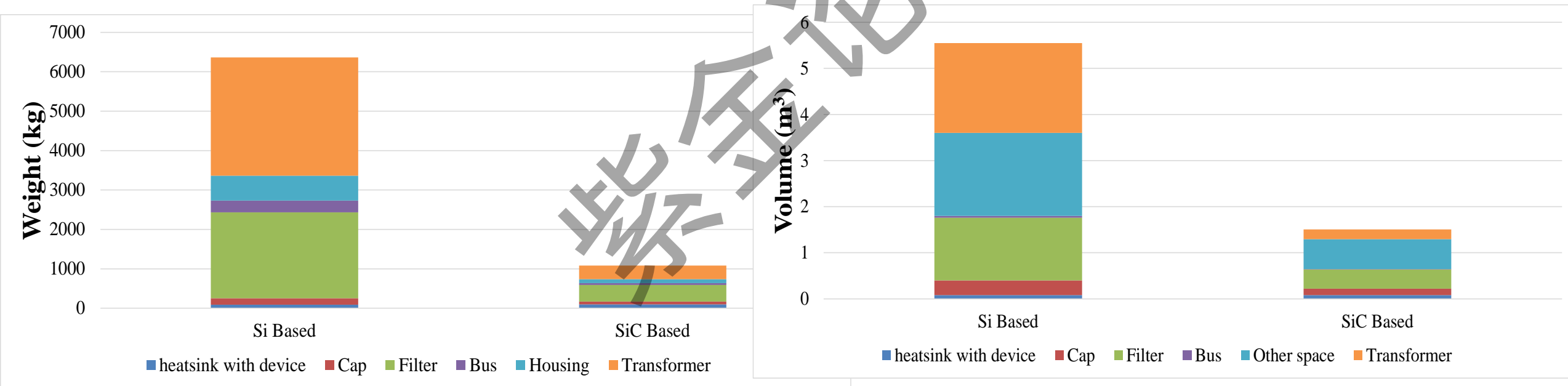
# SiC Impact on PV Inverter

## □ Benchmark study on MV MW-class Si- and SiC-based PV converter design

- Si solution: 2-level with LF transformer (commercial product)
- SiC solution: 3-level NPC & DC/DC with HF transformer (ARPA-e)

## □ Converter comparison

- Weight is reduced by 82.9% (1,088 kg for SiC vs. 6,362 kg for Si)
- Size is reduced by 73.2% (1.49 m<sup>3</sup> for SiC vs. 5.55 m<sup>3</sup> for Si)





# WBG Device Application



Hitachi 160 kW SiC PV inverter

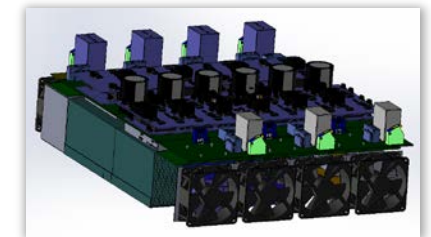
- High voltage rating for higher power
- Low loss for higher efficiency/smaller heatsink
- Fast switching for passive reduction
- High temperature for cooling reduction

3x Specific Power

6x Specific Power



FSU Gen-I SiC PV converter prototype (~2.5 kW/kg, 99% peak efficiency)



FSU Gen-II SiC PV converter conceptual design (~5 kW/kg, 99% peak efficiency)

	Conventional Si inverter		Prototype SiC inverter
Semiconductor Device	Si-IGBT		SiC-MOSFET
Topology	2-level	3-level	2-level
Rating Capacity	100 – 750 kW		160 kW
Peak efficiency of the inverter	98.2% (measured)	97.7 - 98.8% [4,5]	Over 99.1% (measured)

[1] S. Kouro, J. I. Leon, D. Vinnikov and L. G. Franquelo, in *IEEE Industrial Electronics Magazine*, March 2015

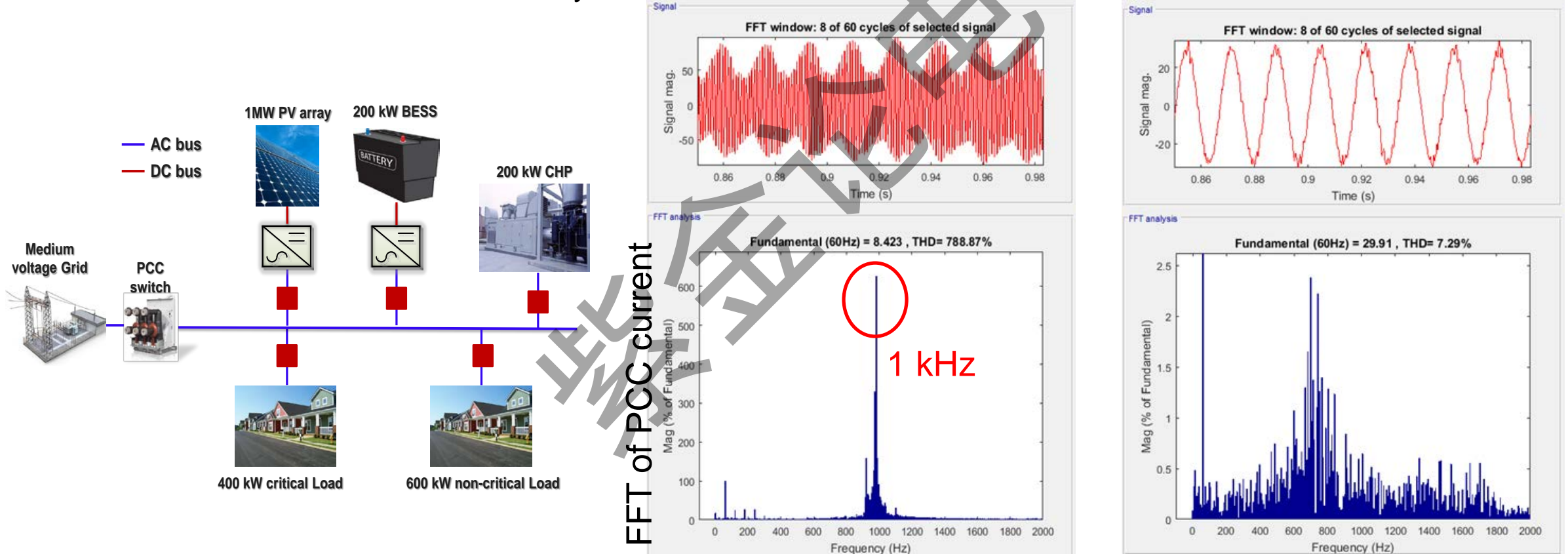
[2] S. Wall, R. h. Ruan, C. g. Wang and J. r. Xie, *EPE'16 ECCE Europe*, Karlsruhe, 2016

[3] A. Hatanaka, H. Kageyama and T. Masuda, *INTELEC*, Osaka, 2015

# System Level Benefits of SiC-Based PV Converter

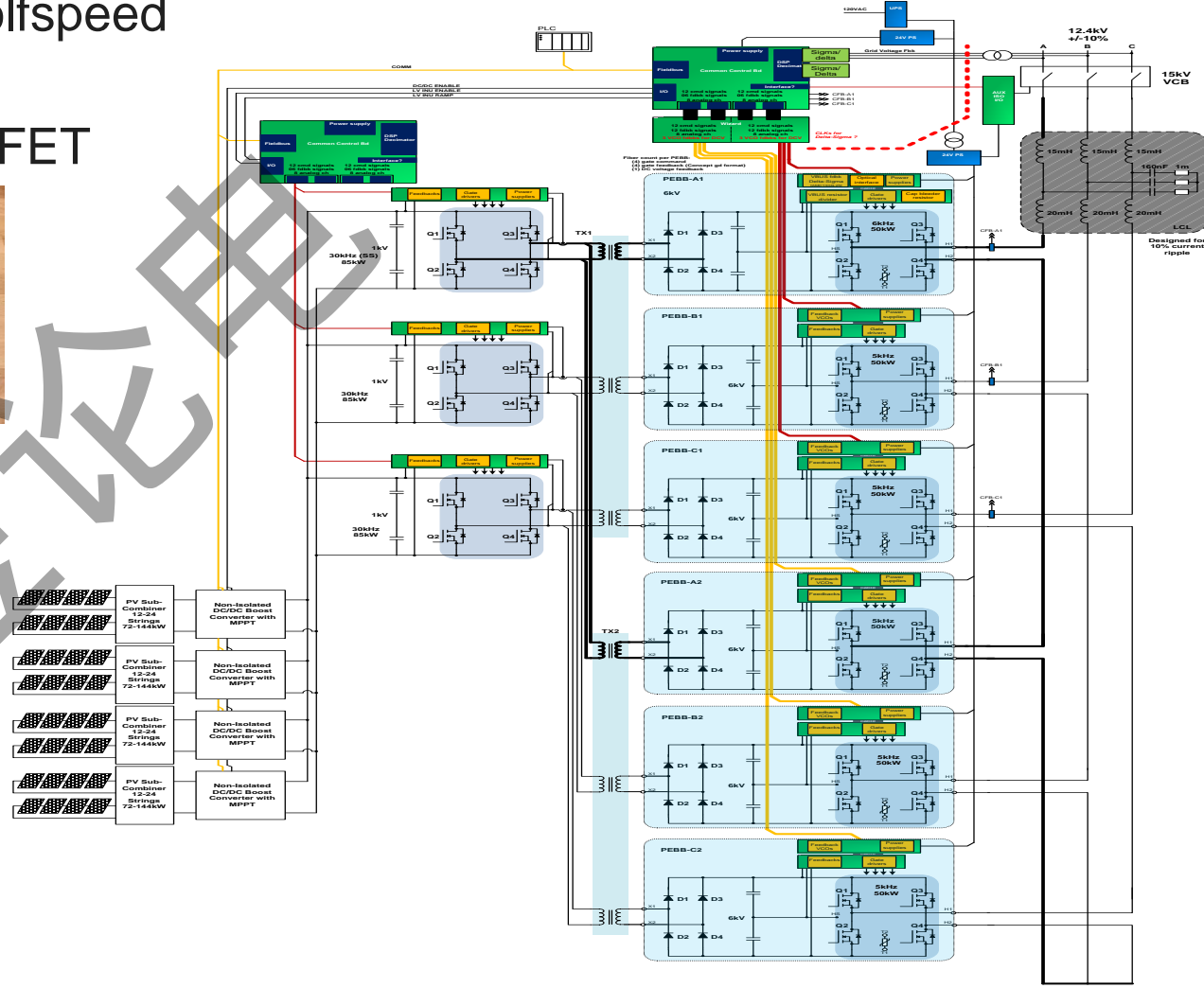
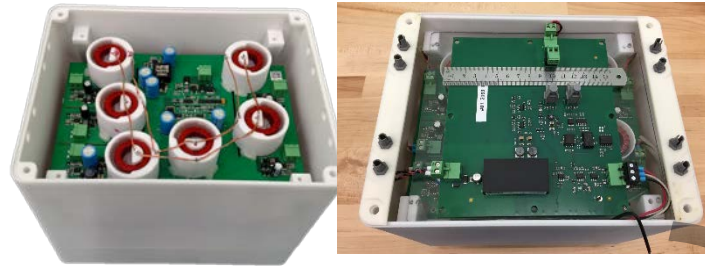
## □ Benchmark study on system level benefits of SiC-based PV converter

- Active filter function with SiC-based PV inverter
- System stability is improved with the higher control bandwidth enabled by MV SiC converter
- SiC-based converter has better dynamic performance during LVRT



# Utility-scale MV Transformer-less PV Inverter

- Supported by ARPA-e and collaborated with Wolfspeed and Danfoss
- 12.4 kV, 1 MW PV inverter with 10 kV SiC MOSFET



MW-scale MV PV inverter topology

# HV SiC-enabled Solid-state Transformer



250 kVA, 20 kHz transformers  
(35-45 kg)

220 kVA/ 330 kVA, 60 Hz transformers



Prototype 1 MW, 4160-V AC/1000-V DC converter



# Research Needs

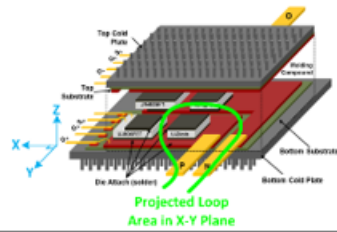
WBG-based converter Issues due to fast switching, high  $dv/dt$  &  $di/dt$ , high voltage, high temperature, high power; unique grid application requirements

Converter	Scaling	Grid Condition Tolerance	Grid Support
<ul style="list-style-type: none"><li>• Device module &amp; packaging</li><li>• Gate drive &amp; protection</li><li>• Parasitics impact</li><li>• Control</li><li>• Passives &amp; filters (<math>dv/dt</math>, <math>di/dt</math>, EMI etc.)</li><li>• Thermal management</li><li>• Insulation (<math>dv/dt</math>, lightning and switching overvoltage)</li><li>• Reliability and management</li></ul>	<ul style="list-style-type: none"><li>• Device module series and paralleling</li><li>• Converter stacking and paralleling</li><li>• Modular topology</li></ul>	<ul style="list-style-type: none"><li>• Faults</li><li>• Unbalance</li><li>• Grounding</li><li>• Fault detection and protection coordination</li></ul>	<ul style="list-style-type: none"><li>• Frequency and voltage support</li><li>• Inertia emulation</li><li>• Low voltage ride through</li><li>• Stability enhancement</li><li>• Active filtering</li><li>• Black start</li></ul>

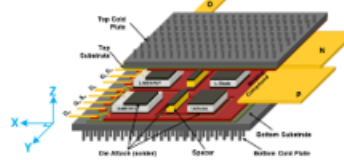
# SiC Module with Low Inductance

Condition (@100MHz)

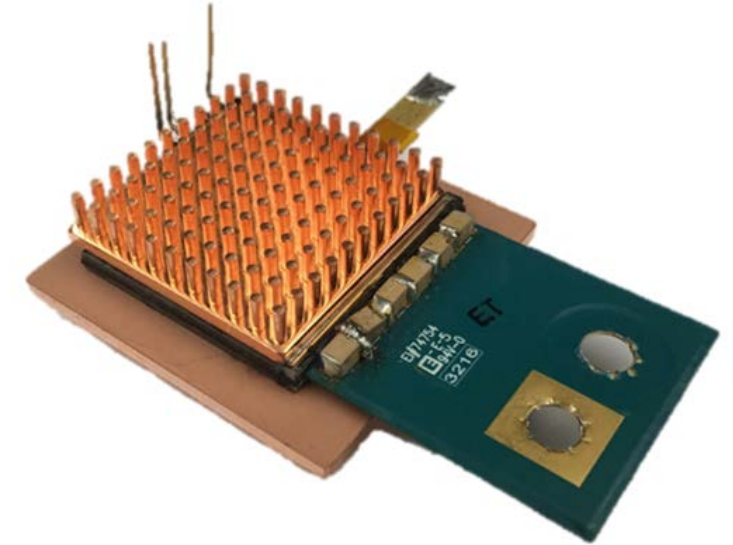
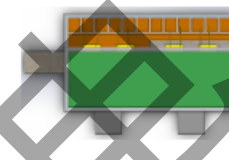
Gen 1 Baseline Module



Gen 2 Module Design



Modified Stack Module

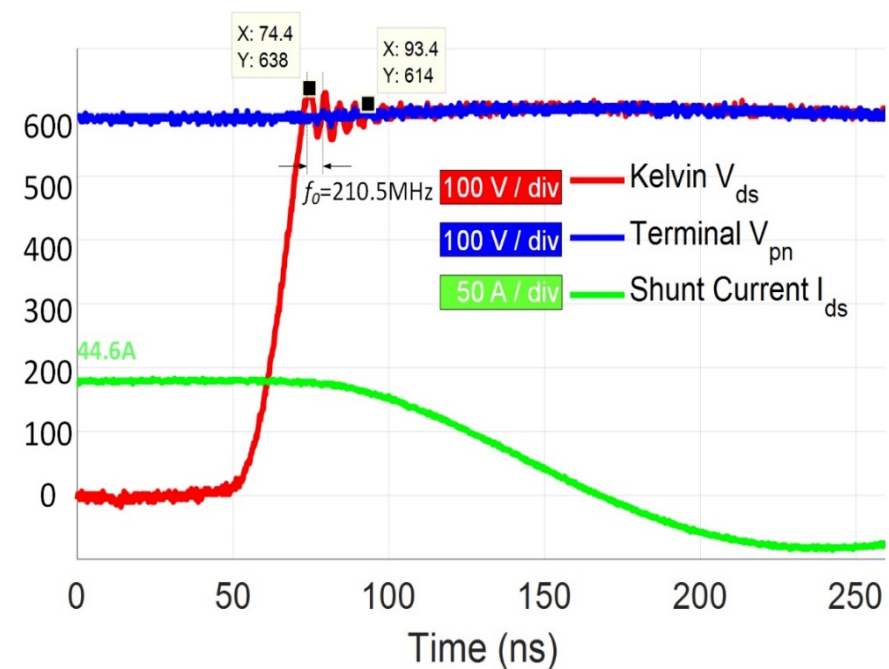
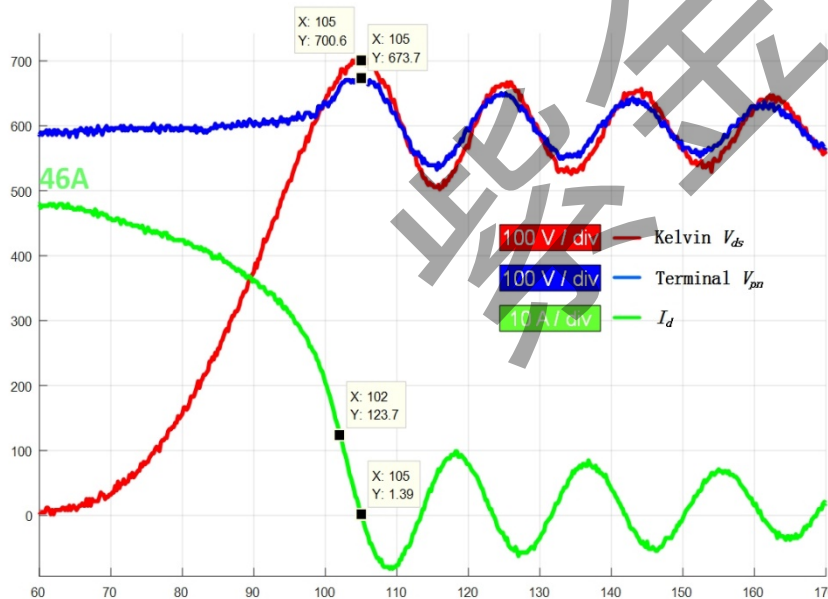


Lower MOSFET Turn off Loop Inductance during di/dt

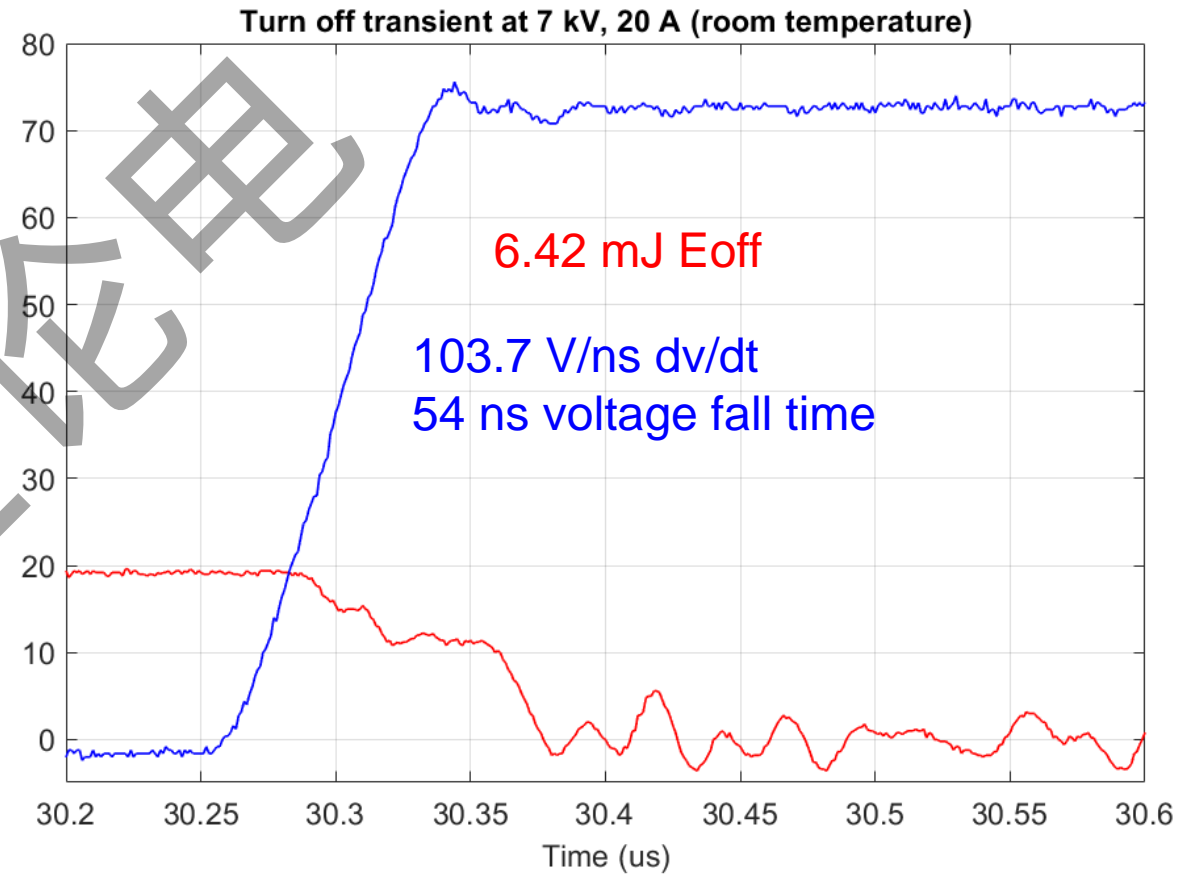
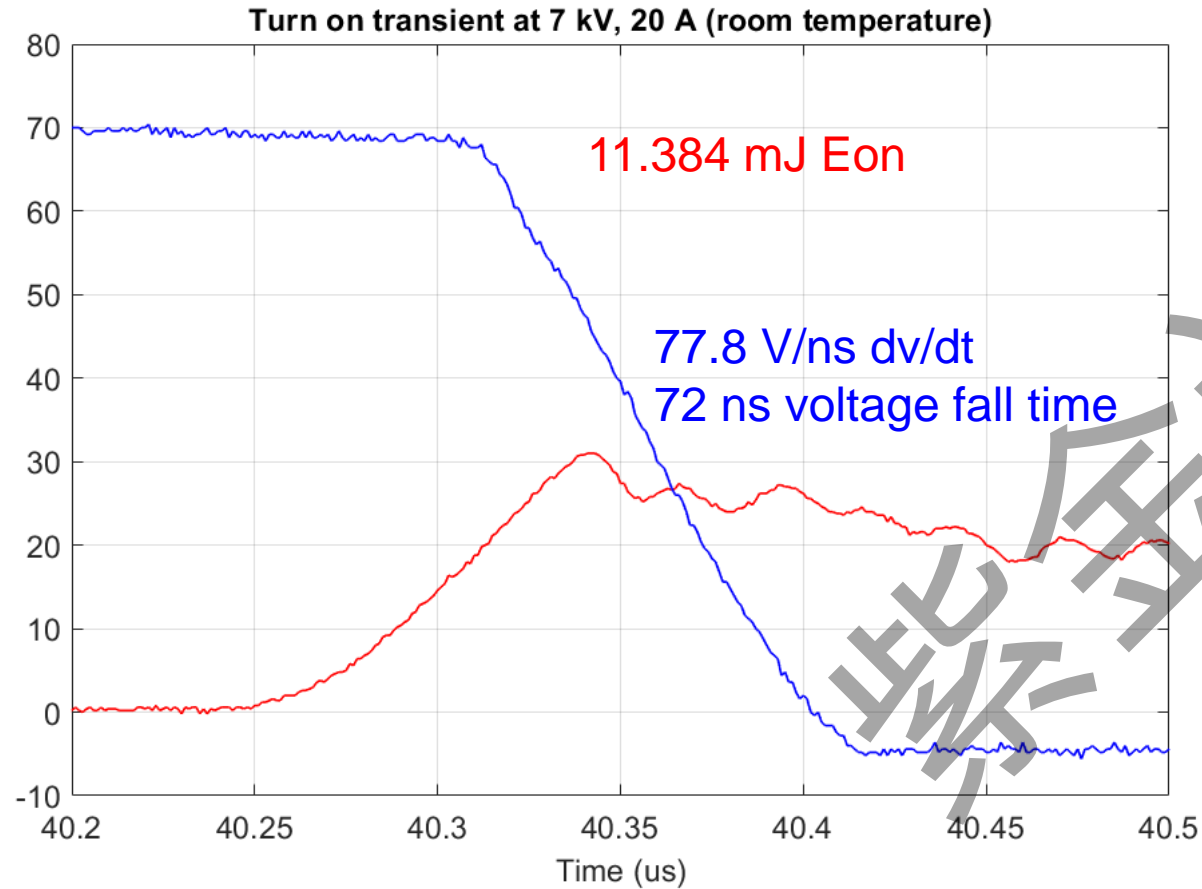
6.03 nH

1.63 nH

1.03 nH

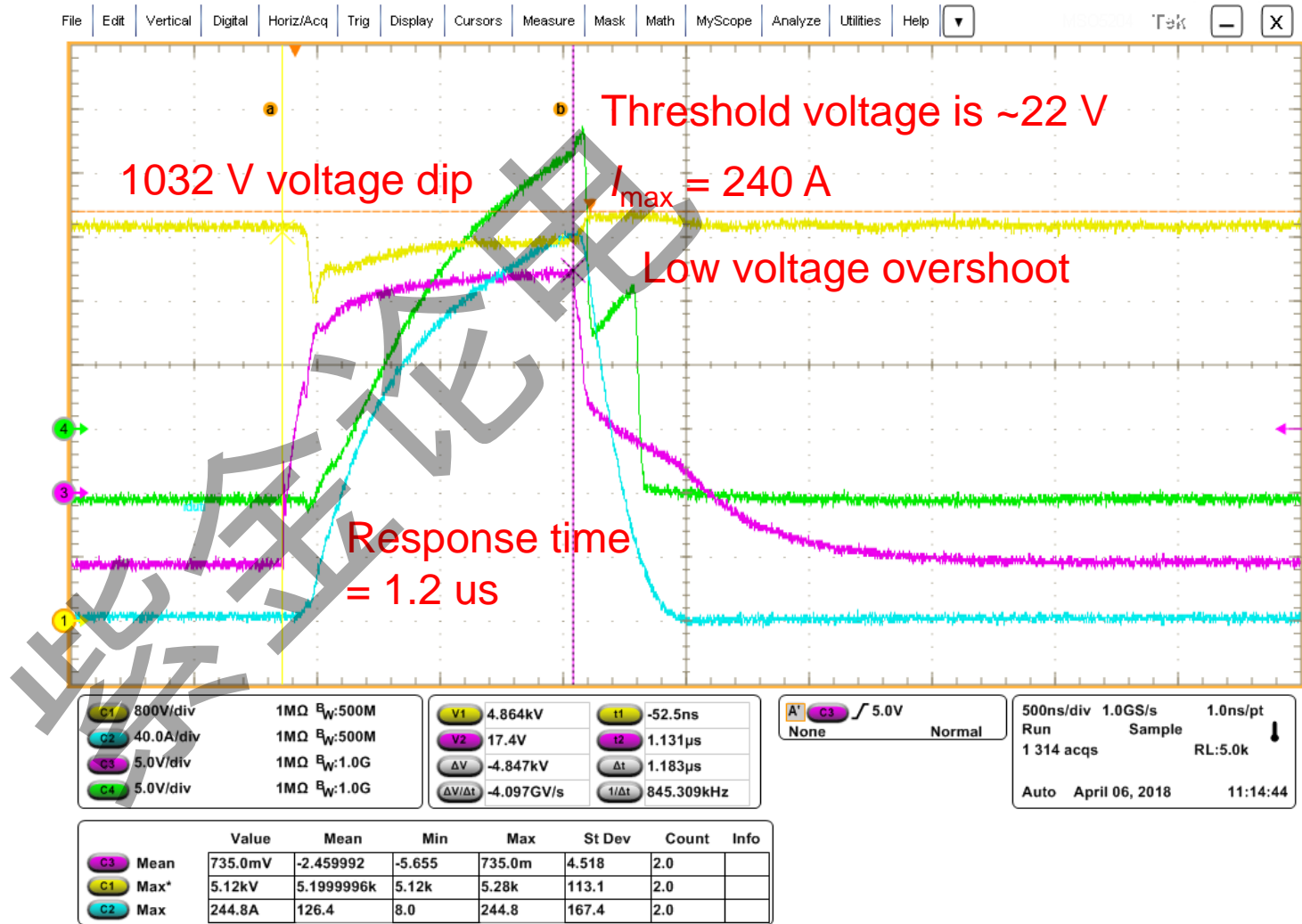


# Switching Results of 10 kV SiC MOSFET



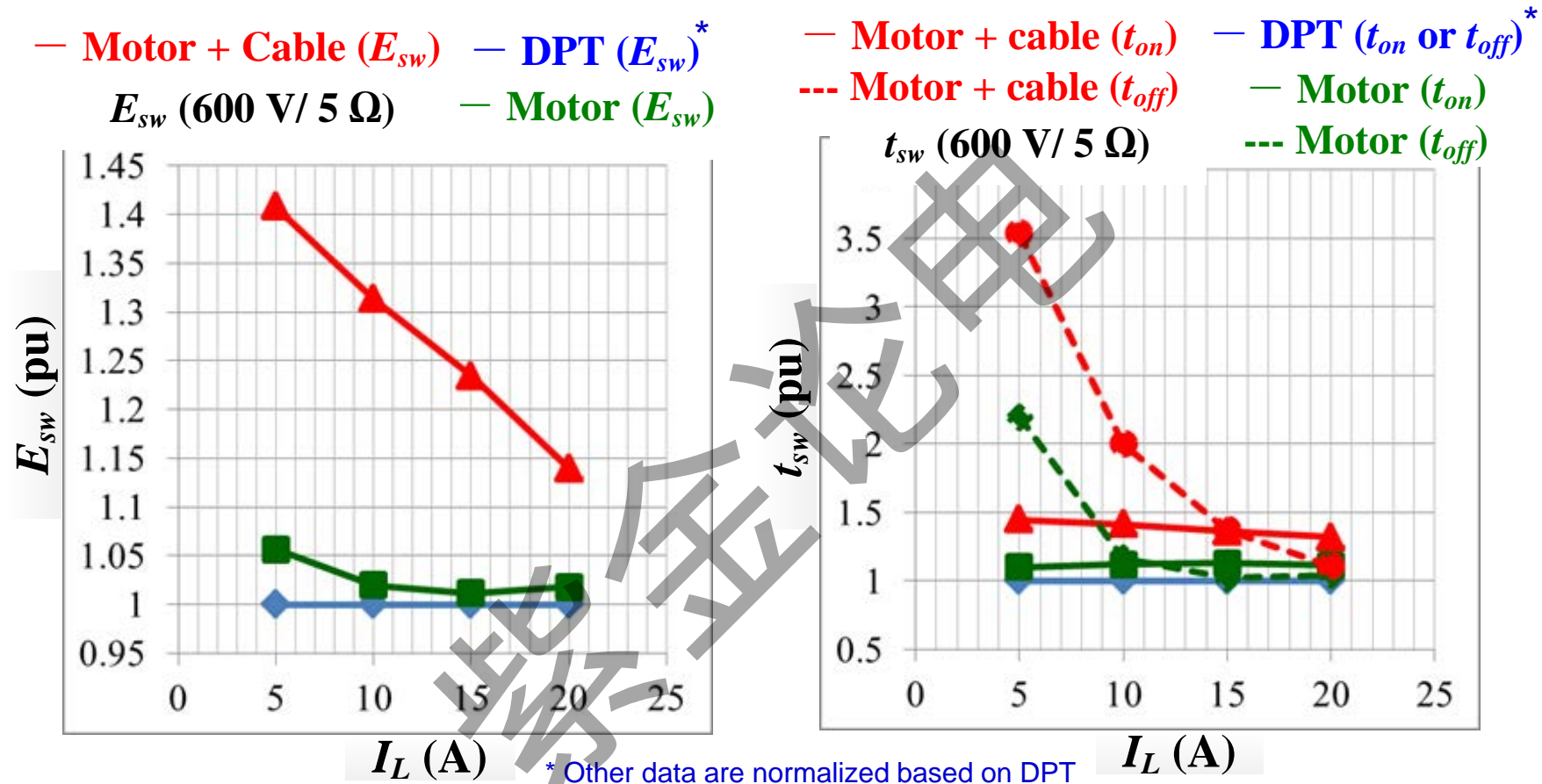
# Short Circuit Protection Test of 10 kV SiC MOSFET

- Ch1: drain-source voltage
- Ch2: drain current
- Ch3: gate-source voltage
- Ch4: monitored Vds in desat circuit



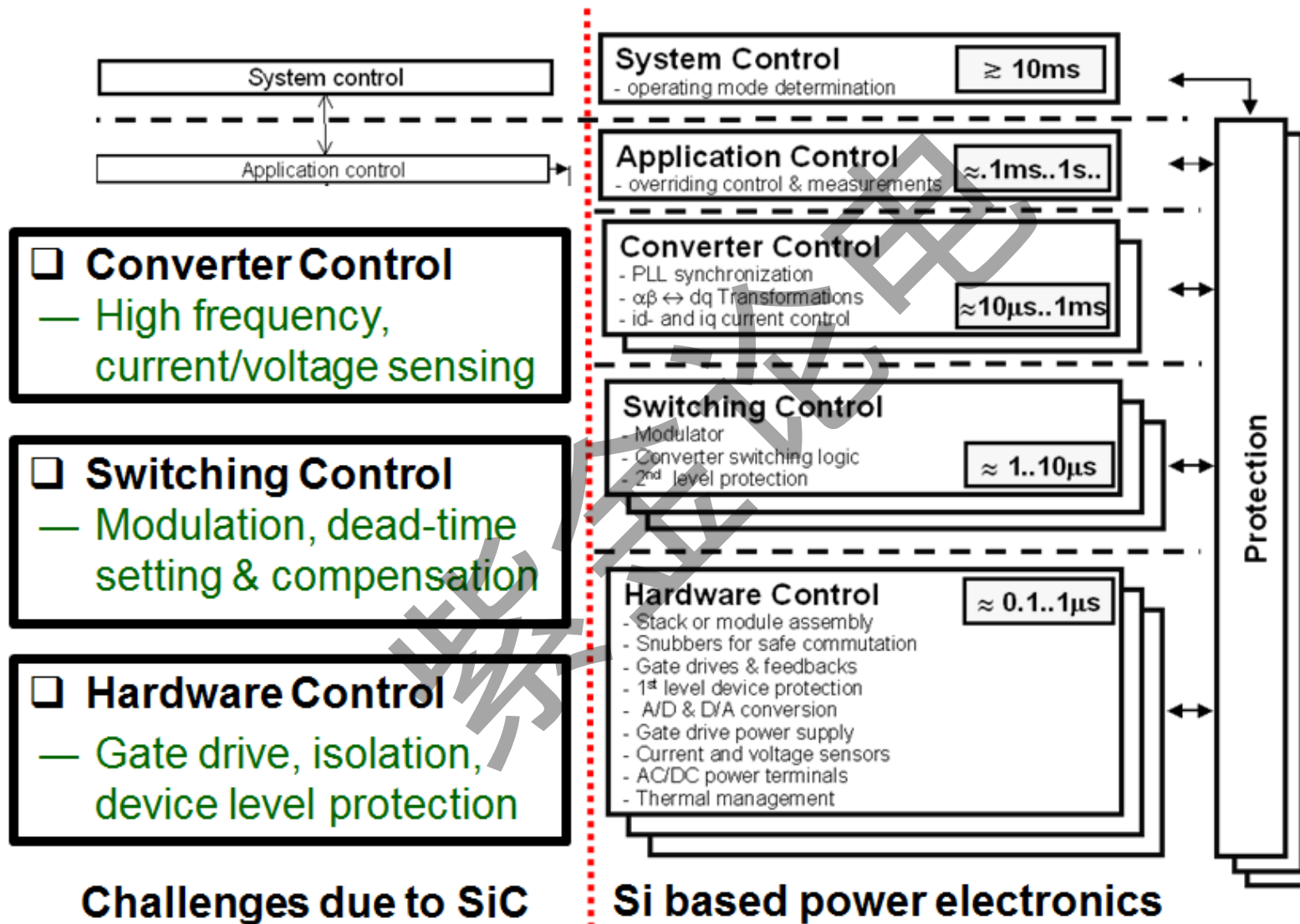


# Impact of Motor & Cable on Drive Design



- ❑ Cooling system cannot be designed based on switching loss from typical DPT
- ❑ Switching frequency and dead time cannot be set based on switching time from DPT

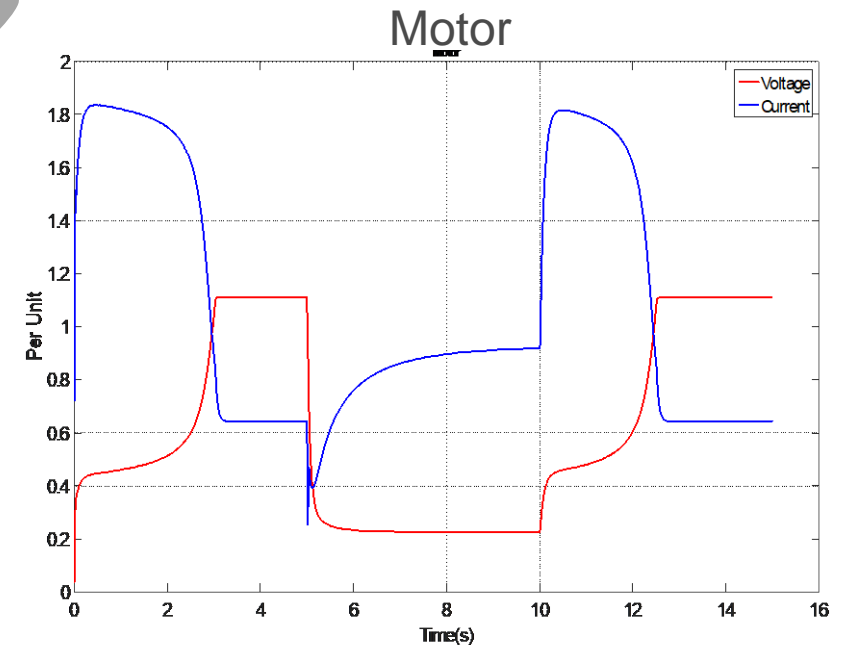
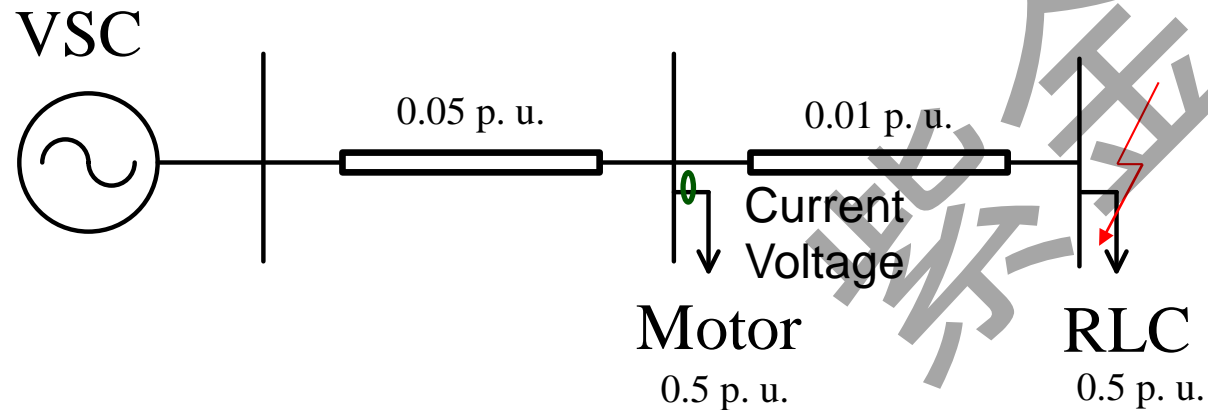
# Control Challenges of WBG Converters



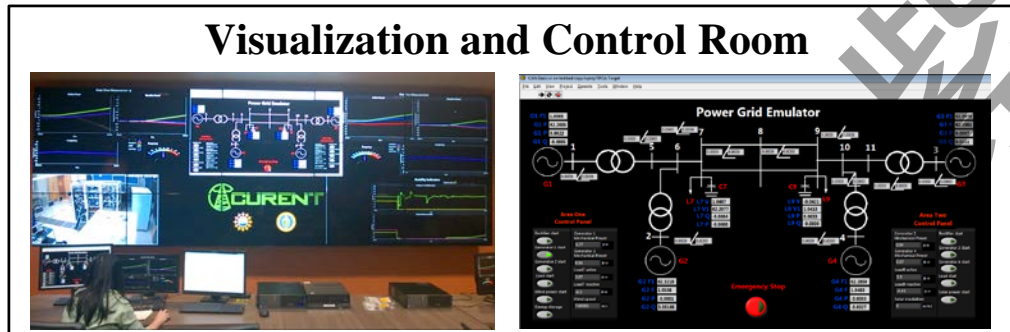
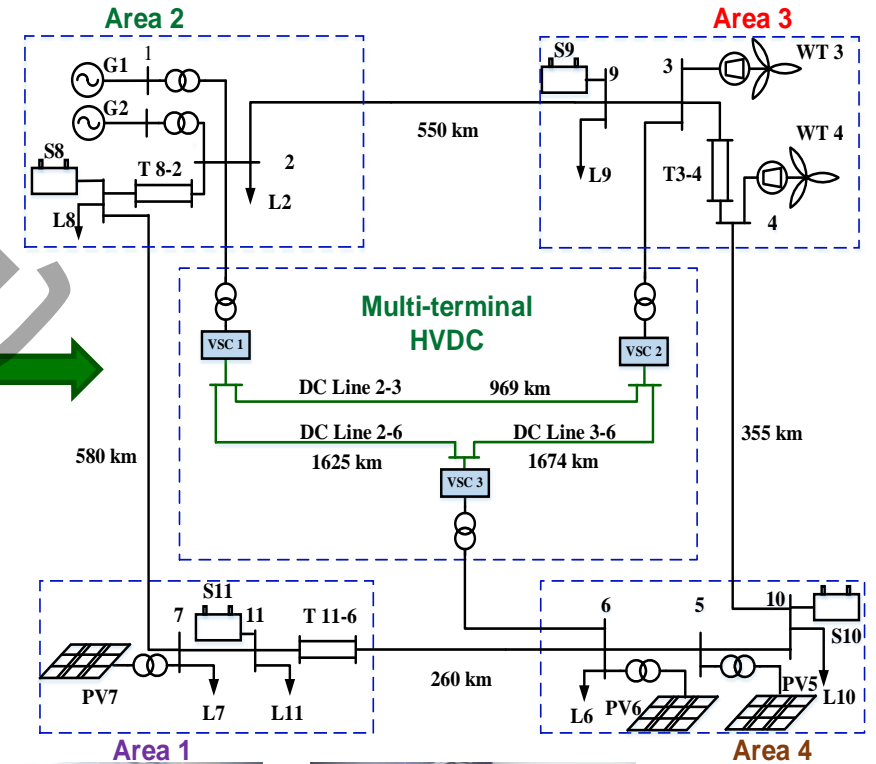
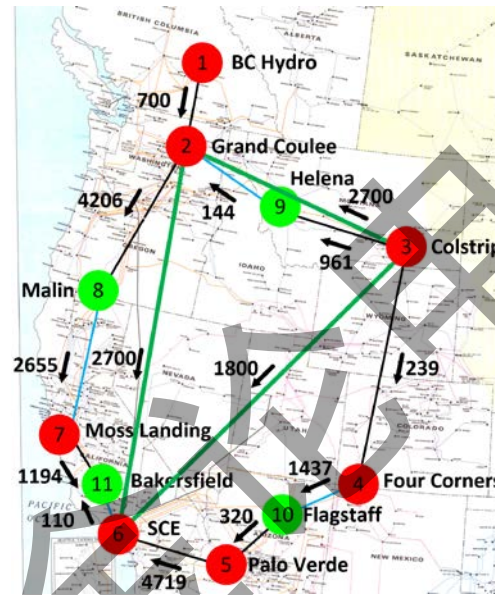
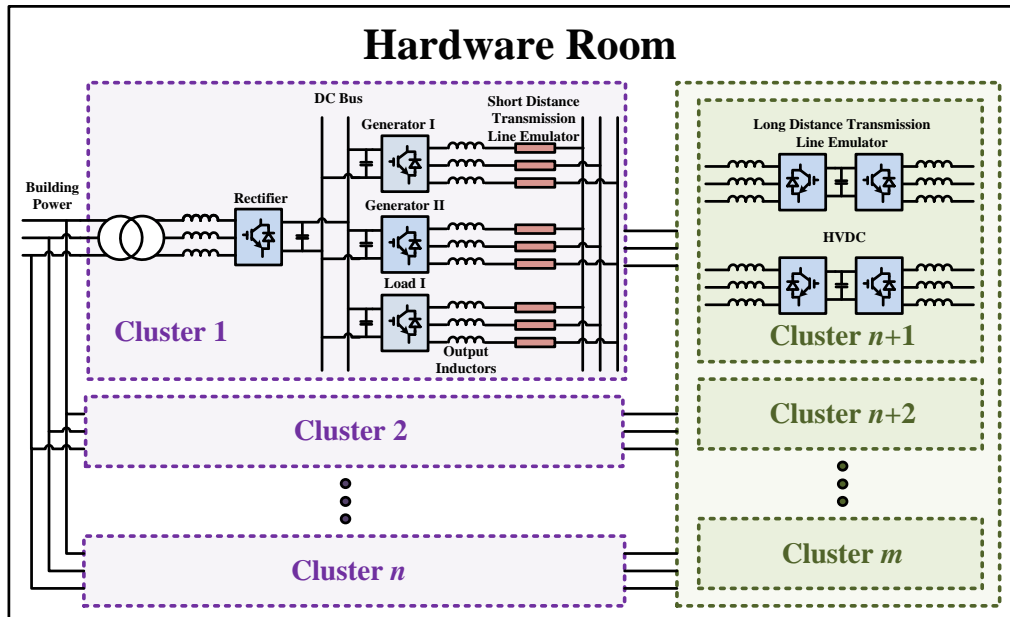
# Converter Fault Current and Protection

## □ Case study of the VSC with 2X rated current capability

- A short-circuit fault can be distinguished from the induction motor start with a time delay shorter than 5s
- Utilizing the bus voltage can distinguish the short-circuit fault from induction motor start within 0.1s
- VSC has similar performances for different fault locations, so a larger time delay is needed to avoid relay protection misoperation
- RLC load branch can distinguish the short-circuit fault quickly based only on the current
- Short-circuit faults within the induction motor branch can be detected quickly by the current



# Power Electronics Grid Emulation Hardware Testbed (HTB)



Front row



Middle row

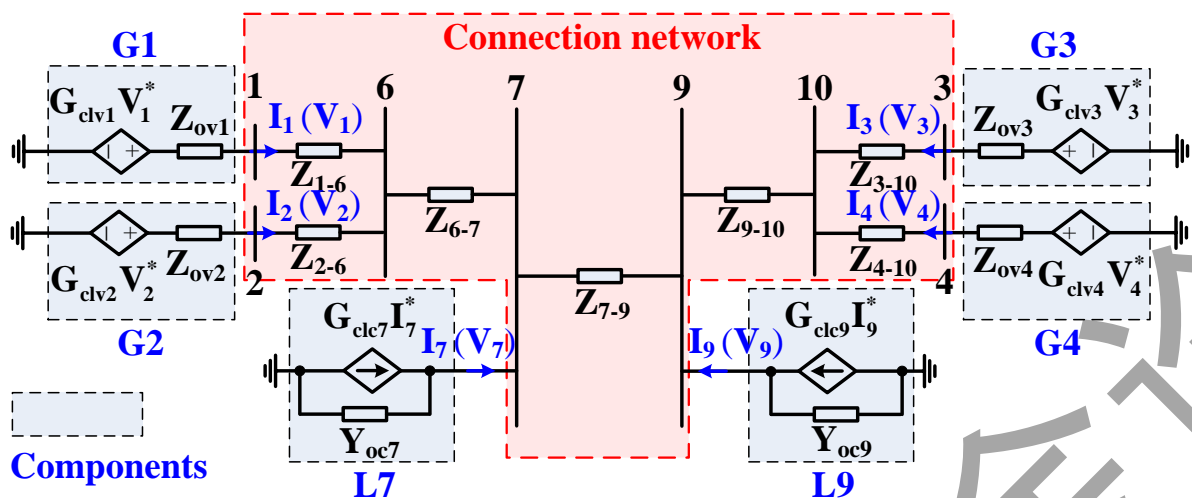


Back row

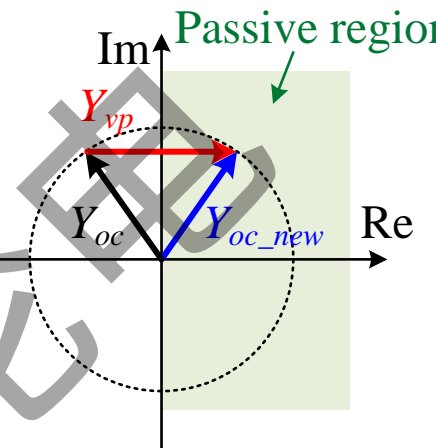


# Renewable Converter Stability Analysis and Controller Design

## Impedance-based stability analysis in $d$ - $q$ frame



## Adaptive passivity compensation

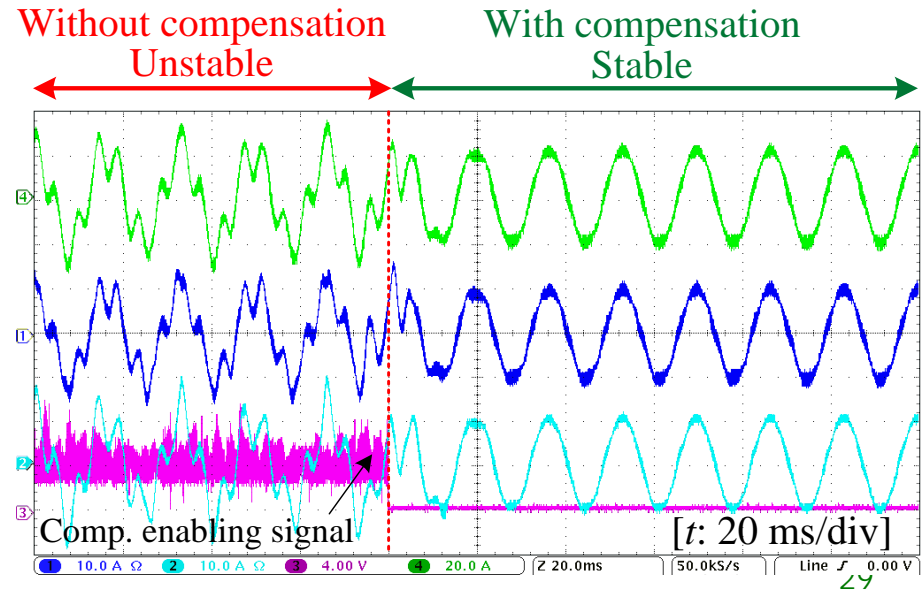
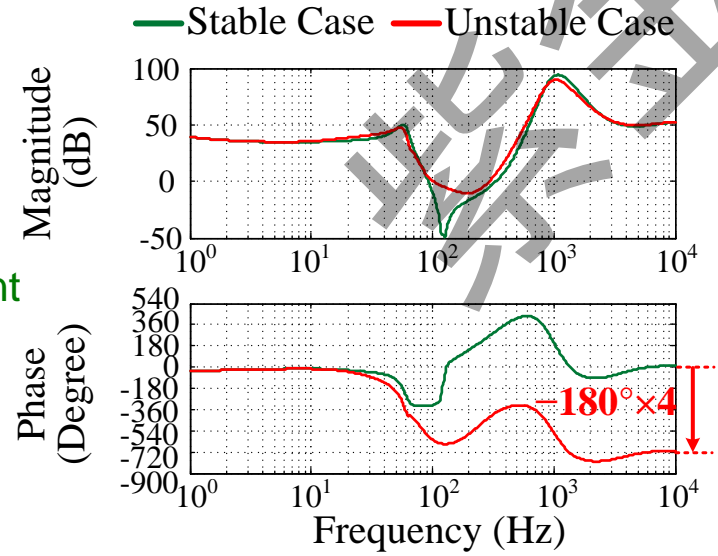


$Y_{oc}$ : inverter admittance without compensation

$Y_{vp}$ : virtual resistance for passivity compensation

$Y_{oc\_new}$ : inverter admittance with compensation

- Component Connection Method
- Generalized Nyquist stability criterion
- Stable if zero encirclement of point  $(0, j0)$
- Avoid pole calculation



# Conclusion

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- The power grid becomes increasingly based on power electronics, due to the changing sources, loads, and T&D technologies.
- WBG devices and other new technologies provide new opportunities for power electronics in grid applications.
- The emerging needs and technologies also pose many new challenges, requiring new paradigm for power electronics and grid.

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